

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	8038	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus and (radiat\$3 heat\$3 thermal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:38
L3	979	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus with (radiat\$3 heat\$3 thermal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:38
L4	3115	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus same (radiat\$3 heat \$3 thermal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:38
L5	6	((substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus and (radiat\$3 heat\$3 thermal)).ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:38
L6	8	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus with (radiat\$3 heat\$3 thermal) and "34"/\$. ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:41
L7	13	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus.ab. with (radiat\$3 heat\$3 thermal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:42

L8	208	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus.ab. and (radiat\$3 heat\$3 thermal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:42
L9	110	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus.ab. and (radiat\$3 heat\$3 thermal)	USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2011/06/29 10:42
L10	3	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus and (radiat\$3 heat\$3 thermal)	FPRS; EPO; JPO	OR	ON	2011/06/29 10:47
L11	8	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus	FPRS; EPO; JPO	OR	ON	2011/06/29 10:47
L12	5	(substrate wafer semiconductor) and (flush\$3 wash\$3 rins \$3) and meniscus not 10	FPRS; EPO; JPO	OR	ON	2011/06/29 10:47

### EAST Search History (Interference)

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**6/ 29/ 2011 10:49:46 AM**